

Department of Electronics and Telecommunication Engineering

**COEP Technological University**

(A Unitary Public University of Government of Maharashtra)

**Time Table**

Class : **B. Tech E & TC**  
With Effect From : 02.01.2023

Academic Year : 2022 - 2023  
Term : II (Second)

Day TIME	10.00 am To 11.00 am	11.00 am To 12.00 pm	12.00 pm To 1.00 pm	1.00 pm To 2.00 pm	2.00 pm To 3.00 pm	3.00 pm To 4.00 pm	4.00 pm To 5.00 pm	5.00 pm To 6.00pm
MON	Project	HONOR-SDN (RAP) ET-002	EL-III M&R ET-003/ ES-RTOS ET- 002/ACMOS ETSH-202	EL-IV NLP ET-001/ BE ETSH-202/ VLSI ET-003		Project		MINOR-CCIP (RVS) ET-002
TUE	Project	HONOR-SDN (RAP) ET-002	EL-III M&R ET-003/ ES-RTOS ET-002/ACMOS ETSH-202	EL-IV NLP ET-001/ BE ETSH-202/ VLSI ET-003		Project		MINOR-CCIP (RVS) ET-002
WED	Project	HONOR-SDN (RAP) ET-002	EL-III M&R ET-003/ ES-RTOS ET- 002/ACMOS ETSH-202	EL-IV NLP ET-001/ BE ETSH-202/ VLSI ET-003		Project		MINOR-CCIP (RVS) ET-002
THU	Project			Project	Project	Project		
FRI	Project			Project	Project	Project		
SAT								

**Department Elective -III** Embedded Software and RTOS (ES-RTOS), Analog CMOS design (ACMOS), Microwave & Radar (M&R)

**Department Elective -IV** Biomedical Engineering (BE), VLSI Design Verification (VLSI), Natural Language Processing (NLP)

**Honor (Software Defined Networks):** Dr. R. A. Patil, **Minor (E&TC- Cloud Computing and IoT Platforms):** Dr. R. V. Sadakale

Timetable In-charge,  
Department of Electronics & Telecommunication Engineering