

COLLEGE OF ENGINEERING, PUNE

Department of E & TC Engineering

Time – Table

Academic Year: 2019-20

Term: First

Class / Division No: M. Tech. (Electronics-VLSI and Embedded Systems)

With effect from: 19.08.2019

TIME	10.00 AM TO 11.00 AM	11.00 AM TO 12.00 PM	12.00 PM TO 1.00 PM	1.00 PM TO 2.00 PM	2.00 PM TO 3.00 PM	3.00 PM TO 4.00 PM	4.00 PM TO 5.00 PM	5.00 PM TO 6.00 PM
PERIOD	1	2	3	4	5	6	7	
MON	PLES Lab (PG Lab-I)		PLES (T)		MAP SH2	Seminar – 15		
TUE	MAP Lab (PG Lab-I)		MAP (T)		RTL SH1	RTL SH1	ML – SH1 HELLA – 15 (4.30 to 6 pm)	
WED	ETC SH1	PGFT SH1	PGFT SH1		PLES 15	RTL 15	DSIP – SH2 HELLA – 15 (4.30 to 6 pm)	
THU		PGFT SH1	PGFT (T) SH1		MAP SH1	MAP SH1	ML - SH1 DSIP – SH2 HELLA – 15 (4.30 to 6 pm)	
FRI	RTL Lab (VLSI Lab)		RTL (T)		PLES 15	PLES 15	ML - SH1 DSIP – SH2 HELLA – 15 (4.30 to 6 pm)	
SAT	RM 10.30 to 12.30 pm on working Saturdays				IPR 1.30 to 3.30 pm on working Saturdays			

PSMC - Probability, Graph and Field Theory (PGFT) (Th + Tut): Dr. Mr. M. S. Sutaone

PSBC - Programming Languages for Embedded Software (PLES) (Th + Lab): Mr. R. V. Sadakale

PCC - RTL Simulation and Synthesis (RTL) (Th + Lab + Tut): Dr. Mrs. V. V. Ingale

Microcontrollers: Architecture and Programming (MAP) (Th + Lab + Tut): Dr. Mr. P. P. Bartakke

DEC - Machine Learning (ML) (Th): Mrs. Y. M. Vaidya

Digital Signal and Image Processing (DSIP) (Th): Dr. Mrs. P. P. Rege

Automotive Embedded Product Development * (HELLA) (Th):

LC - Seminar (Lab): Dr. Mr. P. P. Bartakke

MLC - Research Methodology: Dr. Mr. P. R. Apte

Intellectual Property Rights (IPR): Mrs. Shalaka Deshpande

Effective Technical Communication Skills (ETC) (Th): Dr. Leena Jadhav

Copy to.....

Dean, Academics & QAHead,

In-Charge,
Time – Table Committee