E-5

Mechanical

PROCUREMENT OF GOODS

UNDER

NATIONAL SHOPPING

PROCEDURES

COEP/TEQIP-II/CoE-SRES/March2016/NS/11

For

Cyclone V SoC Development Kit

Bid Price: Nil/-

PACKET NO :COEP/TEQIP-II/ CoE-SRES/ March2016 /NS /11

INVITATION FOR QUOTATIONS FOR SUPPLY OF

Cyclone V SoC Development Kit

1. You are invited to submit your most competitive quotation for the following goods: -

Sr. No	Title /Name of the equipment /System	Brief description [Attach separate annexure if necessary for detailed specifications	Quantity
1	Cyclone V SoC Development Kit	Cyclone V SoC Development Kit Please refer to the Annexure A	One

The schedule is as follows

Date of inviting the quotations	23/03/2016
Last date of submitting the sealed quotation to TEQIP office, COEP	04/04/2016 [upto 3:00 pm]
Opening of the quotations	04/04/2016 [4:00 pm]
Validity of quotation	Min 45 days
Delivery Period	**4 weeks from the acceptance of PO**

2. College of Engineering has received the grants for establishing Center of Excellence in Smart Renewable Energy System under MHRD's Technical Education Quality Improvement Program-Phase II. The said procurement is for this center. This project is World Bank sponsored project. This procurement is being carried out using the National Shopping Process, and will observe the guidelines of Shopping under TEQIP-II.

3. **Bid Price**

    a) The contract shall be for the full quantity as described above and in the annexure. Corrections, if any, shall be made by crossing out, initialing, dating and re-writing.

    b) All duties, taxes and other levies payable by the contractor under the contract shall be included in the total price. However, break- up of the basic price and taxes/duties shall be indicated clearly.

    c) The bidders will be evaluated on the basic price.

d)   The rates quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.

e)   The Prices should be quoted **in Indian Rupees** only.

4.   Each bidder shall submit only one quotation.

5.   **Validity ofQuotation**

Quotation shall remain valid for a period not less than 45 days after the deadline date specified for submission.

6.   **Evaluation of Quotations**

The purchaser shall evaluate and compare the quotations determined to be substantially responsive i.e. which

(a)   are properly signed ; and

(b)   conform to the terms and conditions, and specifications.

The Quotations would be evaluated considering all items together in this packet.

7.   **Award of contract**

The Purchaser shallaward the contract to the bidder whose quotation has been determined to be substantially responsive and who has offered the lowest evaluated quotation price.

7.1   Notwithstanding the above, the Purchaser reserves the right to accept or reject any quotations and to cancel the bidding process and reject all quotations at any time prior to the award of contract.

7.2   The bidder whose bid is accepted shall be notified of the award of contract by the Purchaser prior to expiration of the quotation validity period. The terms of the accepted offer shall be incorporated in the purchase order.

8.   80 % Payment shall be made immediately after delivery of the goods. Remaining 20 % payment will be made after successful commissioning and testing of the equipment/system.

9.   Three years commercial warranty/ guarantee shall be applicable to the supplied goods.

10.   You are requested to provide your offer in sealed envelope latest by **04th April 2016. Please indicate *"Quotation for* Cyclone V SoC Development Kit *CoE-SRES/ March2016 /NS /11" at the right hand corner of the sealed envelope"**

11.   The bidder has to supply the material within the prescribed date. A penalty as per norms will be imposed for delayed supply upto 6 weeks. Any further delay will automatically terminate the purchase order/ contract.

12.   The supplier requires supplying the store exactly as per the specifications and will be responsible to replace the defective supplies at his risk and cost.

13   The Supplier should submit deviation statement if any. The quotations simply mentioning "asper your specification and cost" shall be rejected.

14.   The supplier should arrange for free demo / working trial of equipment (if required) at the Institute / Manufacturers place as the case may be at suppliers cost. The Purchase Order would be placed subject to satisfactory demonstration of the equipment.

15.   Commissioning / Installation is at suppliers cost unless otherwise specified.

16.   Conditional quotation will not be accepted.

17.   We look forward to receiving your quotations and thank you for your interest in this project.

Name: Prof. B. N. Chaudhari
Principal Investigator
Center of Excellence-Smart Renewable Energy System

Annexure A

Detailed technical specifications for Cyclone V SoC Development Kit

Cyclone V SoC Development Kit Contents:
1. The Cyclone V SoC Development Kit
2. The GPIO - HSTC Card (useful for GPIO interfacing)

Features:The Cyclone V SoC Development Kit

- Cyclone V SX development board
  - Featured devices
    - Cyclone V SX SoC—5CSXFC6D6F31C6N (SoC)
    - MAX® V CPLD—5M2210ZF256C4N (system controller)
    - MAX II CPLD—EPM570GF100 (embedded USB-Blaster™ II cable)
  - FPGA configuration sources
    - Embedded USB-Blaster II (JTAG) cable
    - EPCQ flash (PFL)
    - Hard processor system (HPS)
  - FPGA memory
    - 1 GB DDR3 SDRAM (32 bit)
  - FPGA I/O interfaces
    - 2X 10/100 Ethernet PHYs (EtherCAT)
    - PCIe Gen1 x4 female connector
    - Universal high-speed mezzanine card (HSMC)—x4 transceivers, x16 TX LVDS, x16 RX LVDS
    - One serial digital interface (SDI) channel
    - Four SMAs for one transceiver channel
    - 4X push buttons
    - 2X switches
    - 4X LEDs
  - HPS boot sources
    - 128 MB quad serial peripheral interface (SPI) flash memory
    - Removable micro-SD card flash memory
    - FPGA
  - HPS memory
    - 1 GB DDR3 SDRAM (32 bit) with error correction code (ECC)
    - 128 MB quad SPI flash memory
    - Micro-SD card socket with 4 GB micro-SD card flash device
  - HPS I/O interfaces
    - 1X USB 2.0 On-the-Go (OTG)
    - 1X 10/100/1000 Gigabit Ethernet (10GbE/100GbE/1000GbE)
    - 1X CAN
    - 1X UART (UART to USB bridge)
    - 1X real-time clock (with battery backup)
    - 1X two-line text LCD
    - 1-/2-channel, 20 bit delta-sigma analog-to-digital converter (Linear Technology LTC2422)
    - 4X push buttons

- - 4X switches
      - 4X LEDs

    - Clocking
      - Four-output programmable clock generator for FPGA reference clock inputs
      - 125 MHz LVDS oscillator for FPGA reference clock input
      - 148.5 MHz LVDS programmable voltage-controlled crystal oscillator (VCXO) for FPGA reference clock input
      - 50 MHz single-ended oscillator for FPGA and MAX V FPGA clock input
      - 100 MHz single-ended oscillator for MAX V FPGA configuration clock input
      - SMA input for HPS clock
    - Power
      - Laptop DC input 14—20 V adapter
    - System monitoring circuit
      - Power (voltage, current, wattage)
    - HSMC breakout board
    - HSMC loopback board
    - Mechanical
      - Board dimensions—8.19" x 5.22"
- Cyclone V SX FPGA Development Kit software content (downloadable from Table 2)
- Design examples
    - Board test system (BTS)*
    - Golden System Reference Design with Board Update Portal web server
- [SoC Embedded Design Suite Standard Edition](#)
    - ARM Development Studio 5 (DS-5™) Altera Edition Toolkit
    - Hardware-to-software handoff tools
    - Linux run-time software for application development
    - SoC hardware libraries for firmware development
    - Application examples