PROCUREMENT OF GOODS
UNDER
NATIONAL SHOPPING
PROCEDURES

COEP/TEQIP-II/CoE-SRES/March2016/NS/12

For

Zybo Zynq-7000 ARM/FPGA SoC Trainer Board

Bid Price: Nil/-

PACKET NO :COEP/TEQIP-II/ CoE-SRES/ March2016 /NS /12

INVITATION FOR QUOTATIONS FOR SUPPLY OF

Zybo Zynq-7000 ARM/FPGA SoC Trainer Board

1.   You are invited to submit your most competitive quotation for the following goods: -

Sr. No	Title /Name of the equipment /System	Brief description [Attach separate annexure if necessary for detailed specifications	Quantity
1	Zybo Zynq-7000 ARM/FPGA SoC Trainer Board	Zybo Zynq-7000 ARM/FPGA SoC Trainer Board Please refer to the Annexure A	One

The schedule is as follows

Date of inviting the quotations	23/03/2016
Last date of submitting the sealed quotation to TEQIP office, COEP	04/04/2016 [upto 3:00 pm]
Opening of the quotations	04/04/2016 [4:00 pm]
Validity of quotation	Min 45 days
Delivery Period	4 weeks from the acceptance of PO

2.   College of Engineering has received the grants for establishing Center of Excellence in Smart Renewable Energy System under MHRD's Technical Education Quality Improvement Program-Phase II. The said procurement is for this center. This project is World Bank sponsored project. This procurement is being carried out using the National Shopping Process, and will observe the guidelines of Shopping under TEQIP-II.

3.   **Bid Price**

   a)   The contract shall be for the full quantity as described above and in the annexure. Corrections, if any, shall be made by crossing out, initialing, dating and re-writing.

   b)   All duties, taxes and other levies payable by the contractor under the contract shall be included in the total price. However, break- up of the basic price and taxes/duties shall be indicated clearly.

   c)   The bidders will be evaluated on the basic price.

d) The rates quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.

e) The Prices should be quoted **in Indian Rupees** only.

4. Each bidder shall submit only one quotation.

5. **Validity ofQuotation**

Quotation shall remain valid for a period not less than 45 days after the deadline date specified for submission.

6. **Evaluation of Quotations**

The purchaser shall evaluate and compare the quotations determined to be substantially responsive i.e. which

(a) are properly signed ; and

(b) conform to the terms and conditions, and specifications.

The Quotations would be evaluated considering all items together in this packet.

7. **Award of contract**

The Purchaser shallaward the contract to the bidder whose quotation has been determined to be substantially responsive and who has offered the lowest evaluated quotation price.

7.1 Notwithstanding the above, the Purchaser reserves the right to accept or reject any quotations and to cancel the bidding process and reject all quotations at any time prior to the award of contract.

7.2 The bidder whose bid is accepted shall be notified of the award of contract by the Purchaser prior to expiration of the quotation validity period. The terms of the accepted offer shall be incorporated in the purchase order.

8. 80 % Payment shall be made immediately after delivery of the goods. Remaining 20 % payment will be made after successful commissioning and testing of the equipment/system.

9. Three years commercial warranty/ guarantee shall be applicable to the supplied goods.

10 You are requested to provide your offer in sealed envelope latest by **04th April 2016. Please indicate** *"Quotation for   Zybo Zynq-7000 ARM/FPGA SoC*

Trainer Board CoE-SRES/ March2016 /NS /12" at the right hand corner of the sealed envelope"

11.   The bidder has to supply the material within the prescribed date. A penalty as per norms will be imposed for delayed supply upto 6 weeks. Any further delay will automatically terminate the purchase order/ contract.

12.   The supplier requires supplying the store exactly as per the specifications and will be responsible to replace the defective supplies at his risk and cost.

13   The Supplier should submit deviation statement if any. The quotations simply mentioning "asper your specification and cost" shall be rejected.

14.   The supplier should arrange for free demo / working trial of equipment (if required) at the Institute / Manufacturers place as the case may be at suppliers cost. The Purchase Order would be placed subject to satisfactory demonstration of the equipment.

15.   Commissioning / Installation is at suppliers cost unless otherwise specified.

16.   Conditional quotation will not be accepted.

17.   We look forward to receiving your quotations and thank you for your interest in this project.

Name: Prof. B. N. Chaudhari
Principal Investigator
Center of Excellence-Smart Renewable Energy System

Annexure A

Detailed technical specifications for Zybo Zynq-7000 ARM/FPGA SoC Trainer Board

Accessories:
- Voucher for Xilinx Vivado Design Suite: Design Edition
  (One full seat, node locked, device-locked to the Zynq-7000 XC7Z010)
- 4GB microSD card
- Micro USB cable
- 5V 2.5A switching power supply

Features:
- Xilinx Zynq-7000 (XC7Z010-1CLG400C)
- 28,000 logic cells
- 240 KB Block RAM
- 80 DSP slices
- On-chip dual channel, 12-bit, 1 MSPS analog-to-digital converter (XADC)
- 650 MHz dual-core Cortex™-A9 processor
- On-board JTAG programming and UART to USB converter
- DDR3 memory controller with 8 DMA channels
- 512 MB x32 DDR3 w/ 1050Mbps bandwidth
- 128 Mb Serial Flash w/ QSPI interface
- microSD slot (supports Linux file system)
- High-bandwith peripheral controllers: 1G Ethernet, USB 2.0, SDIO
- Low-bandwidth peripheral controller: SPI, UART, I2C
- Dual-role (Source/Sink) HDMI port
- 16-bits per pixel VGA output port
- Trimode (1Gbit/100Mbit/10Mbit) Ethernet PHY
- OTG USB 2.0 PHY (supports host and device)
- External EEPROM (programmed with 48-bit globally unique EUI-48/64™ compatible identifier)
- Audio codec with headphone out, microphone and line in jacks
- GPIO: 6 pushbuttons, 4 slide switches, 5 LEDs

Six Pmod connectors (1 processor-dedicated, 1 dual analog/digital)