

COLLEGE OF ENGINEERING, PUNE-05
DEPARTMENT OF COMPUTER ENGINEERING AND INFORMATION TECHNOLOGY
Tentative Timetable

S. Y. Btech Computer Engineering - Diploma

Semester – IV

2020-21

W.E.F. 04/01/2021

Venue: Different Online Classrooms

Time Slot	DAY →	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
	TIME						
1	9:00-9:50	LAUC(9:00-9:50)	LAUC(9:00-9:50)	LAUC(9:00-9:50)	LAUC(9:00-9:50)	LAUC(9:00-9:50)	
2	9:50-10:00	Break					
3	10:00-10:50	FP(10:00-11:00)	FP(10:00-11:00)	FP(10:00-11:00)	FP(10:00-11:00)	Professional Laws, Ethics, Values and Harmony	
4	10:50-11:00	Break					
5	11:00-11:50	DSA-I	DSA-I	DSA-I	DLD	DLD	S5: DSA1-Lab
6	11:50-12:00						
7	12:00-12:50	TOC	TOC	TOC	DC	S5: TOC-Tut	
8	12:50-1:30	Lunch Break					
9	1:30-3:20	DLD(1:30-2:20)	S5:IFC-SA-Lab		S5:DLD-LAB	S5: RPPOOP-Lab	
10	3:20-3:30	Break					
11	3:30-4:20			DC	RPPOOP	BE	
12	4:20-4:30	Break					
13	4:30-5:20	IFC-SA	DC			BE	
14	5:20-5:30	Break					
15	5:30-6:20			BE	BE		

Faculty Assignment for Courses, Short Names
S. Y. Btech Computer Engineering - Diploma

SUBJECT	FACULTY
LAUC	
FP	
DSA-I	Shrida Kalamkar
TOC	Pravin Pawar
DLD	Jagruti Waykole
DC	Deepak Kshirsagar
RPPOOP	Sunil Mane
SA	
BE	

Time Table Incharge

LAB	Assignment of Batches to Faculty
DSA- I Lab	Shrida Kalamkar
DLD	Jagruti Waykole
RPPOOP	Sunil Mane
SA Lab	

Short Name	Subject
DSA-I	Data Structures & Algorithms-I
DLD	Digital Logic Design
BE	Biology for Engineers
TOC	Theory of Computation
SA	Sensors and Automation (IFC)
DC	Data Communication
RPPOOP	Rapid Prototyping Practice Using Object Oriented Programming
LAUC	Linear Algebra and Univariate Calculus(DSY)
FP	Foundation of Physics (DSY)

HOD

