

COLLEGE OF ENGINEERING, PUNE-05
DEPARTMENT OF COMPUTER ENGINEERING AND INFORMATION TECHNOLOGY

S. Y. Btech Computer Engineering - Div 1

Semester – III

2021-22

W.E.F. 09/01/2022

Venue: AC-203

	0	1	3	4	5	6	7	9	10	12
	09:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00	06:00-07:00
MON			DLD	ODEMC	Lunch Break	S1: DTL-Lab S2: DSA1-Lab S3: DLD-Lab S4:PPL-Lab		DSA1		
TUE			DLD	ODEMC		S2: DTL-Lab S3: DSA1-Lab S4: DLD-Lab S5:PPL-Lab		DSA1		
WED		PPL	DLD	ODEMC Tut B2-AC-203, B1-PL Lab, B3-GL Lab		S3: DTL-Lab S4: DSA1-Lab S5: DLD-Lab S1:PPL-Lab		DSGT Tut		
THU		PPL	FCS	DSGT		S4: DTL-Lab S5: DSA1-Lab S1: DLD-Lab S2:PPL-Lab		FCS-Tut		
FRI	PLEV	PPL	DTL	DSGT		S5:DTL-Lab S1: DSA1-Lab S3:PPL-Lab		S2:DLD-Lab	S2: DLD-Lab	
SAT				Innovation(12:30-01:30)						

Faculty Assignment for Courses, Short Names
S. Y. Btech Computer Engineering - Div 1

SUBJECT	FACULTY	LAB	Assignment of Batches to Faculty	Lab Venu	ShortName	Subject
PPL	Prof. Vaibhav K Khatavkar	PPL	S1 to S5: Prof. Vaibhav Khatavkar	PL Lab	PPL	Principles of Programming Languages
ODEMC	Prof. Pallavi Shikhare	DSA1	S1 to S5: Prof. Ashwini Matange	FOSS1 Lab	ODEMC	Ordinary Differential Equations and Multivariate Calculus
PLEV		DLD	S1 to S5: Prof. Pravin Pawar	DLD Lab	PLEV	Professional Laws, Ethics and Values
I&C		DTL	S1 to S5: Prof. Trishna Ugale	Database Lab	I&C	Innovation and Creativity Development Tools Laboratory
DTL	Prof. Trishna Ugale	DSGT Tut	Prof. Rohini Sarode	Cogni E Lab	DTL	
FCS	Prof. P. D. Shendge	FCS Tut	Prof. P. D. Shendge	DLD Lab	FCS	Feedback Control Systems
DSA1	Prof. Ashwini Matange				DSA1	Data Structures and Algorithms - 1
DLD	Prof. Revankar				DLD	Digital Logic Design
DSGT	Prof. Rohini Sarode				DSGT	Discrete Structures and Graph Theory

Time Table Incharge

HOD

COLLEGE OF ENGINEERING, PUNE-05
DEPARTMENT OF COMPUTER ENGINEERING AND INFORMATION TECHNOLOGY

S. Y. Btech Computer Engineering - Div 2

Semester – III

2021-22

W.E.F. 09/01/2022

Venue: AC-202

	0	1	3	4	5	6	7	9	10	12
	09:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00	01:00-02:00	02:00-03:00	03:00-04:00	04:00-05:00	05:00-06:00	06:00-07:00
MON	LAUC	FP		Lunch Break						
	S1:DTL-Lab DSA1-Lab S3: DLD-Lab S4:PPL-Lab	S2:	PPL		ODEMC	DSA1	PLEV	S5: DLD-Lab		
TUE	LAUC	FP								
	S3:DTL-Lab DSA1-Lab S1:PPL-Lab	S4:	PPL		ODEMC	DSA1		S5:PPL-Lab		
WED	LAUC	FP								
	S2:DTL-Lab DSA1-Lab S4: DLD-Lab S1: DSGT-Tut (10:00 to 11:00)	S3:	DTL		ODEMC Tut B1-AC-202, B2-PL Lab, B3-GL Lab	DLD		S5: DSA1-Lab		
THU	LAUC	FP								
	S4:DTL-Lab DLD-Lab S2:PPL-Lab	S1:	DLD		DSGT		DSGT-Tut	S5: DTL-Lab S1: DSA1-Lab S2: DLD-Lab S3:PPL-Lab		
FRI	LAUC	FCS	DLD		DSGT	FCS-Tut		PPL		
SAT				Innovation(12:30-01:30)						

Faculty Assignment for Courses, Short Names
S. Y. Btech Computer Engineering - Div 2

SUBJECT	FACULTY	LAB	Assignment of Batches to Faculty	Lab Venu	ShortName	Subject
PPL	Prof. Vaibhav K Khatavkar	PPL	S1 to S5: Prof. Rahul Adhao	PL Lab	PPL	Principles of Programming Languages
ODEMC	Prof. Pralhad Shinde	DSA	S1 to S5: Prof. Pratiksha Deshmukh	FOSS1 Lab	ODEMC	Ordinary Differential Equations and Multivariate Calculus
PLEV		DLD	S1 to S3: Prof. Revankar S4&S5: Prof. Kushal Khairnar	DLD Lab	PLEV	Professional Laws, Ethics and Values
I&C		DTL	S1 to S2: Prof. Trishna Ugale S3 to S5: Prof. Pravin Pawar	Database Lab	I&C	Innovation and Creativity
DTL	Prof. Trishna Ugale	DSGT Tut	Prof. Vibhavari Kamble	Cogni E Lab	DTL	Development Tools Laboratory
FCS	Prof. C. Y. Patil	FCS Tut	Prof. C. Y. Patil	DLD Lab	FCS	Feedback Control Systems
DSA1	Prof. Pratiksha Deshmukh				DSA1	Data Structures and Algorithms - 1
DLD	Prof. Revankar				DLD	Digital Logic Design
DSGT	Prof. Vibhavari Kamble				DSGT	Discrete Structures and Graph Theory
FP					FP	Foundation of Physics

Time Table Incharge

HOD