

COLLEGE OF ENGINEERING, PUNE-05
DEPARTMENT OF COMPUTER ENGINEERING AND INFORMATION TECHNOLOGY
Tentative Timetable

S. Y. Btech Computer Engineering - Div 1

Semester – III

2020-21

W.E.F. 03/08/2020

Venue: Different Online Classrooms

	0	1	2	3	4	5	6	7	8	9	10	8	12
	09:00-09:45	09:45-10:30	Break	11:00-11:45	11:45-12:30	Lunch Break	1:30-2:15	2:15-3:00	Break	3:30-4:15	4:15-5:00	Break	5:15-6:00
MON		PLEV		ODEMC AC-203	DLD		S1: DTL-Lab S2: DSA1-Lab S3: DLD-Lab S4:PPL-Lab			DSA1	PPL		S1: DSGT-Tut S2: FCS-Tut
TUE				ODEMC AC-203	DLD		S2: DTL-Lab S3: DSA1-Lab S4: DLD-Lab S5:PPL-Lab			DSA1	PPL		S2: DSGT-Tut S3: FCS-Tut
WED				ODEMC AC-203	DSGT		S3: DTL-Lab S4: DSA1-Lab S5: DLD-Lab S1:PPL-Lab			PPL	DTL		S3: DSGT-Tut S4: FCS-Tut
THU				ODEMC AC-203	DSGT		S4: DTL-Lab S5: DSA1-Lab S1: DLD-Lab S2:PPL-Lab			PPL	DLD		S4: DSGT-Tut S5: FCS-Tut
FRI		FCS		DSA1	DSGT		S5: DTL-Lab S1: DSA1-Lab S2: DLD-Lab S3:PPL-Lab				DLD		S5: DSGT-Tut S1: FCS-Tut
SAT				Innovation(11:30-12:15)									

Faculty Assignment for Courses, Short Names
S. Y. Btech Computer Engineering - Div 1

SUBJECT	FACULTY
PPL	Vaibhav K Khatavkar
ODEMC	J T Majithia
PLEV	Kshipra Moghe
I&C	
DTL	J V Aghav
FCS	
DSA1	Shreeda Kalamkar
DLD	Pravin Pawar
DSGT	Vibhavari Kamble

Time Table Incharge

LAB	Assignment of Batches to Faculty
PPL	S1 to S5: Vaibhav K Khatavkar
DSA1	S1 to S5: Shreeda Kalamkar
DLD	S1 to S5: Pravin Pawar
DTL	S1 to S5: J V Aghav
DSGT Tut	Vibhavari Kamble

HOD

Short Name	Subject
PPL	Principles of Programming Languages
ODEMC	Ordinary Differential Equations and Multivariate Calculus
PLEV	Professional Laws, Ethics and Values
I&C	Innovation and Creativity
DTL	Development Tools Laboratory
FCS	Feedback Control Systems
DSA1	Data Structures and Algorithms - 1
DLD	Digital Logic Design
DSGT	Discrete Structures and Graph Theory

COLLEGE OF ENGINEERING, PUNE-05
DEPARTMENT OF COMPUTER ENGINEERING AND INFORMATION TECHNOLOGY
Tentative Timetable

S. Y. Btech Computer Engineering - Div 2

Semester – III

2020-21

W.E.F. 03/08/2020

Venue: Different Online Classrooms

	0	1	2	3	4	5	6	7	8	9	10	8	12
	09:00-09:45	09:45-10:30	Break	11:00-11:45	11:45-12:30	Lunch Break	1:30-2:15	2:15-3:00	Break	3:30-4:15	4:15-5:00	Break	5:15-6:00
MON	LAUC	FP		ODEMC	DLD		S1: DTL-Lab S2: DSA1-Lab S3: DLD-Lab S4:PPL-Lab			DSA1	PPL		S1: DSGT-Tut S2: FCS-Tut
TUE	LAUC	FP		ODEMC	DLD		S2: DTL-Lab S3: DSA1-Lab S4: DLD-Lab S5:PPL-Lab			DSA1	PPL		S2: DSGT-Tut S3: FCS-Tut
WED	LAUC	FP		ODEMC	DSGT		S3: DTL-Lab S4: DSA1-Lab S5: DLD-Lab S1:PPL-Lab			PPL	DTL		S3: DSGT-Tut S4: FCS-Tut
THU	LAUC	FP		ODEMC	DSGT		S4: DTL-Lab S5: DSA1-Lab S1: DLD-Lab S2:PPL-Lab			PPL	DLD		S4: DSGT-Tut S5: FCS-Tut
FRI	LAUC	FCS		PLEV	DSGT		S5: DTL-Lab S1: DSA1-Lab S2: DLD-Lab S3:PPL-Lab			DSA1	DLD		S5: DSGT-Tut S1: FCS-Tut
SAT				Innovation(11:30-12:15)									

Faculty Assignment for Courses, Short Names
S. Y. Btech Computer Engineering - Div 2

SUBJECT	FACULTY
PPL	Hirve
ODEMC	J.T. Majithia
PLEV	Kshipra Moghe
I&C	
DTL	Deepak D Kshirsagar
FCS	
DSA1	Ashwini Matange
DLD	Jagruti Waykole
DSGT	Rohini Sarode
FP	
LAUC	

Time Table Incharge

LAB	Assignment of Batches to Faculty
PPL	S1 to S5: Sumit Hirve
DSA	S1 to S5: Ashwini Matange
DLD	S1 to S5: Jagruti Waykole
DTL	S1 to S5: Deepak D Kshirsagar
DSGT Tut	Rohini Sarode

HOD

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ODEMC	Ordinary Differential Equations and Multivariate Calculus
PLEV	Professional Laws, Ethics and Values
I&C	Innovation and Creativity
DTL	Development Tools Laboratory
FCS	Feedback Control Systems
DSA1	Data Structures and Algorithms - 1
DLD	Digital Logic Design
DSGT	Discrete Structures and Graph Theory
FP	Foundation of Physics
LAUC	Linear Algebra and Univariate Calculus