

DEPARTMENT OF ELECTRICAL ENGINEERING COLLEGE OF ENGINEERING, PUNE Time – Table										
Academic Year – 2016-17 Term – Second Class Room:- EE 101						Class- S.Y.B.Tech. With effect from – 1/2/2017				
TIME	08.00 AM TO 09.00 AM	09.00 AM TO 10.00 AM	10.00 AM TO 11.00 AM	11.00 AM TO 12.00 PM	12.00 PM TO 01.00 PM	01.00 PM TO 02.00 PM	02.00 PM TO 03.00 PM	03.00 PM TO 04.00 PM	04.00 PM TO 05.00 PM	05.00 PM TO 06.00 PM
PERIOD	1	2	3	4	5	6	7	8	9	10
MON			VC & PDE	EMF		DS & CP Lab A E Mc Lab B DE Lab C E Mc Tut D (1-2) (EE 103) EMF Tut D (2-3) (EE 103)			E Mc I	
TUE			VC & PDE	DE		DS & CP Lab B E Mc Lab C DE Lab D E Mc Tut A (1-2)(EE 103) EMF Tut A (2-3) (EE 103)		S of LS	EMF	
WED			ILOE	EMF		DS & CP Lab C E Mc Lab D DE Lab A E Mc Tut B (1-2) (EE 101) EMF Tut B (2-3) (EE 101)		VC & PDE Tut (EE 101, 102, 103)		
THR			ILOE	DE		DS & CP Lab D E Mc Lab A DE Lab B E Mc Tut C (1-2) (EE 103) EMF Tut C (2-3) (EE 103)		S of LS	E Mc I	
FRI			ILOE	DE			E Mc I	S of LS	DS & CP	
SAT										

Subjects :

Electrical Machinery I (E M/C I): Dr. R. T. Ugale

Digital Electronics (DE): Prof. Shreyas Deshmukh

Electro Magnetic Fields (EMF): Dr. A.P.Deshpande

Electro Magnetic Fields Tutorial (EMF TUT) : Dr. A.P.Deshpande

Electrical Machinery I Tutorial (E M/C I TUT): Prof. Pratap Kumar K.

Digital Electronics Lab : Prof. Shreyas Deshmukh & Prof. Rohan Kulkarni

Electrical Machinery I Lab: Dr. R. T. Ugale & Dr. B. N. Chaudhari

Data Structure & Computer Programming Lab: Dr.M.Murali (Batch A, C, B) & Prashant (Batch D)

Data Structure & Computer Programming (DS & CP) : Dr.Mrs.M.Murali