

DEPARTMENT OF ELECTRICAL ENGINEERING
COLLEGE OF ENGINEERING, PUNE
Time – Table

Academic Year – 2016-17

Term – Second

Class Room:- EE 104

Class- T.Y.B.Tech.

With effect from – 1/2/2017

TIME	08.00	09.00	10.00	11.00	12.00	01.00	02.00	03.00	04.00	05.00
	AM TO 09.00 AM	AM TO 10.00 AM	AM TO 11.00 AM	AM TO 12.00 PM	PM TO 01.00 PM	PM TO 02.00 PM	PM TO 03.00 PM	PM TO 04.00 PM	PM TO 05.00 PM	PM TO 06.00 PM
PERIOD	1	2	3	4	5	6	7	8	9	10
MON			CS Tut B (EE 104) PE Tut A (PE lab)	CS I		PS II	PS II	CS I Lab-A PS II Lab-B PE Lab-C DSP Lab-D		
TUE			PE	PE Tut B (PE lab) CS Tut C (EE 104)		PSP	DSP	CS I Lab-B PS II Lab-C PE Lab-D DSP Lab-A		
WED			PE	PE Tut C (PE lab) CS Tut D (EE 104)		CS I	PSP	PS II	ILOE	
THR			PE Tut D (PE lab)	CS I		CS Tut A (EE 104)	CS I Lab-C PS II Lab-D PE Lab-A DSP Lab-B		ILOE	
FRI			PE	PS II		CS I Lab-D PS II Lab-A PE Lab-B DSP Lab-C		PSP	ILOE	
SAT										

Subjects :

Control Systems-I : Prof. A. V. Tare

Power Systems-II : Dr. A. A. Dharme

Power Electronics : Dr. D. B. Talange

Power System Protection : Dr. U. M. Mate

Control System Tutorial: Dr. S S Dambhare & Prof. Bhagyashri Tamhane

Power Electronics Tutorial : Dr. D. B. Talange & Prof. Rohan Kulkarni

Power Systems Lab-II : Prof. T. P. Hinge and Dr. S. S. Dambhare

Control Systems Lab- I : Prof. A. V. Tare

Power Electronics Lab: Dr. V. S. Rajguru

Digital Signal Processing Lab: Prof. Shreyas Deshmukh